

ANALOG DEVICES PCN 11_0198 MODIFICATIONS TO NON-LINEARITY FOR DAC08A DEVICE TYPE 02 ONLY, (5962-89932 QMLR & 38510/113 JAN S DEVICES)

FOR PART NUMBERS 5962R8993202VEA (DAC08AQ/QMLR), 5962R8993202V2A (DAC08ARC/QMLR), & 5962R8993202VFA (DAC08AF/QMLR) only								
TEST	SYMBOL	CONDITIONS Vs = ± 15V, Iref = 2 mA	GROUP A SUBGROUPS	Change From		Change To		UNITS
				MIN	MAX	MIN	MAX	
Non-Linearity	NL		1,2,3		± 0.1		± 0.11	% FS

FOR PART NUMBER M38510/11302SEA (DAC08SA Q5) JANS only								
TEST	SYMBOL	CONDITIONS ±Vcc = ±15Vdc, source resistance = 50Ω, Iref = 2.0 mA.	GROUP A SUB-GROUPS	Change From		Change To		UNITS
				MIN	MAX	MIN	MAX	
Positive Bit Errors	$\Sigma NL+$	Measure Io, $\frac{\Sigma \text{Positive bit errors}}{IFS}$	1,2,3		0.10		0.11	%
	$\overline{\Sigma NL+}$	Measure \overline{Io} , $\frac{\Sigma \text{Positive bit errors}}{IFS}$			0.10		0.11	
Negative Bit Errors	$\Sigma NL-$	Measure Io, $\frac{\Sigma \text{Negative bit errors}}{IFS}$	1,2,3	-0.10		-0.11		%
	$\overline{\Sigma NL-}$	Measure \overline{Io} , $\frac{\Sigma \text{Negative bit errors}}{IFS}$		-0.10		-0.11		
Positive & Negative Bit Error Difference	$\Delta \Sigma NL$	Measure Io, $ \Sigma NL+ - \Sigma NL- $	1,2,3	-0.03	0.03	-0.033	0.033	%
	$\overline{\Delta \Sigma NL}$	Measure \overline{Io} , $ \overline{\Sigma NL+} - \overline{\Sigma NL-} $		-0.03	0.03	-0.033	0.033	
Positive Relative Accuracy	NL+	Measure Io, $ \Sigma NL+ + \Delta \Sigma NL $	1,2,3	0	0.10	0	0.11	%
	$\overline{NL+}$	Measure \overline{Io} , $ \overline{\Sigma NL+} + \overline{\Delta \Sigma NL} $		0	0.10	0	0.11	
Negative Relative Accuracy	NL-	Measure Io, $ \Sigma NL- + \Delta \Sigma NL $	1,2,3	0	0.10	0	0.11	%
	$\overline{NL-}$	Measure \overline{Io} , $ \overline{\Sigma NL-} + \overline{\Delta \Sigma NL} $		0	0.10	0	0.11	
Para.1.2.1	Device types: Change description from “Device type 02 D/A Converter, 8 bit, 0.10% Linearity” Change to: Device type 02 “D/A Converter, 8 bit, 0.11% Linearity”							

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